

FIG. 1

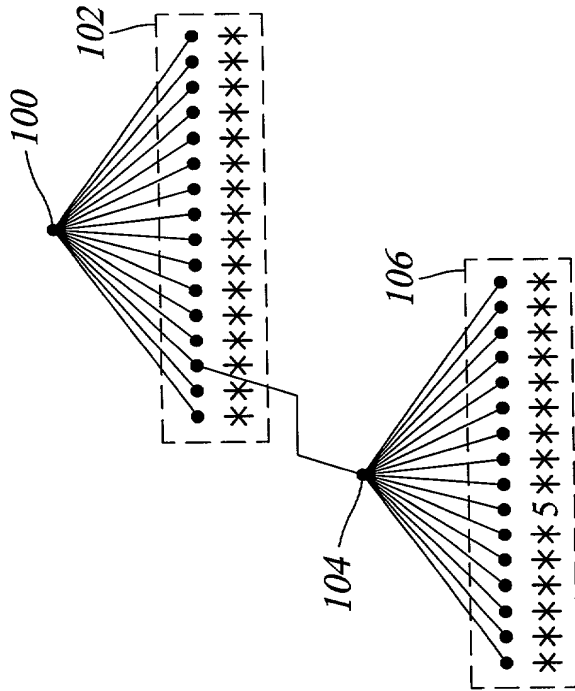


FIG. 2

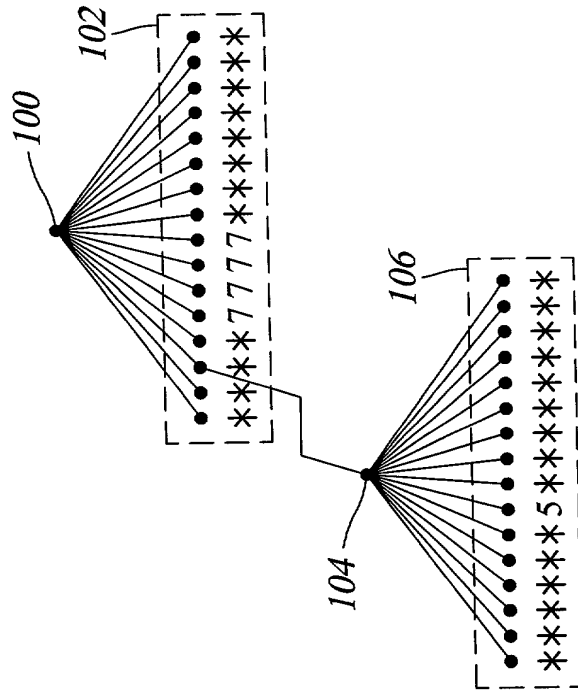


FIG. 3



FIG. 4

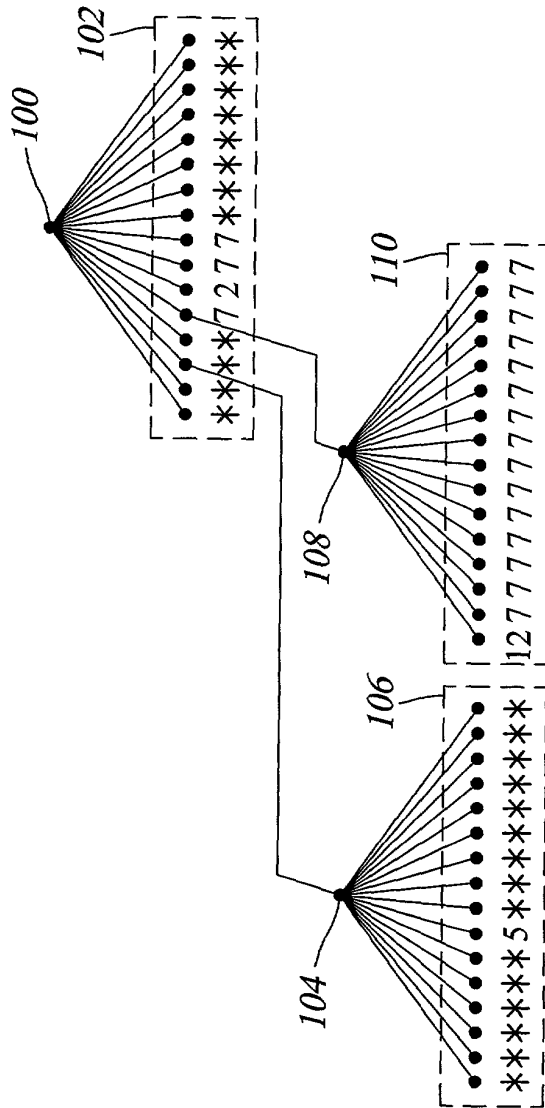


FIG. 5

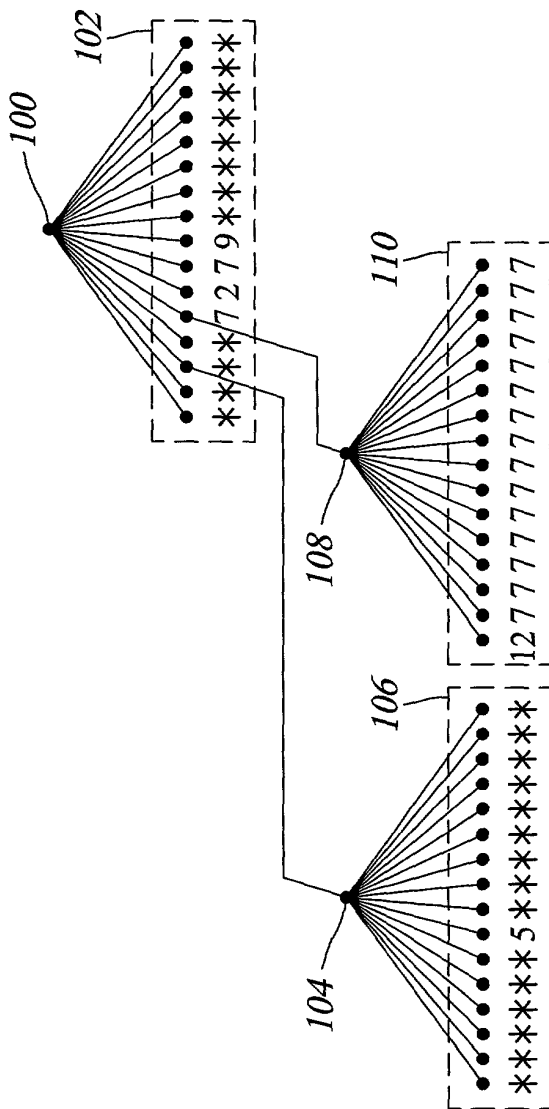


FIG. 6

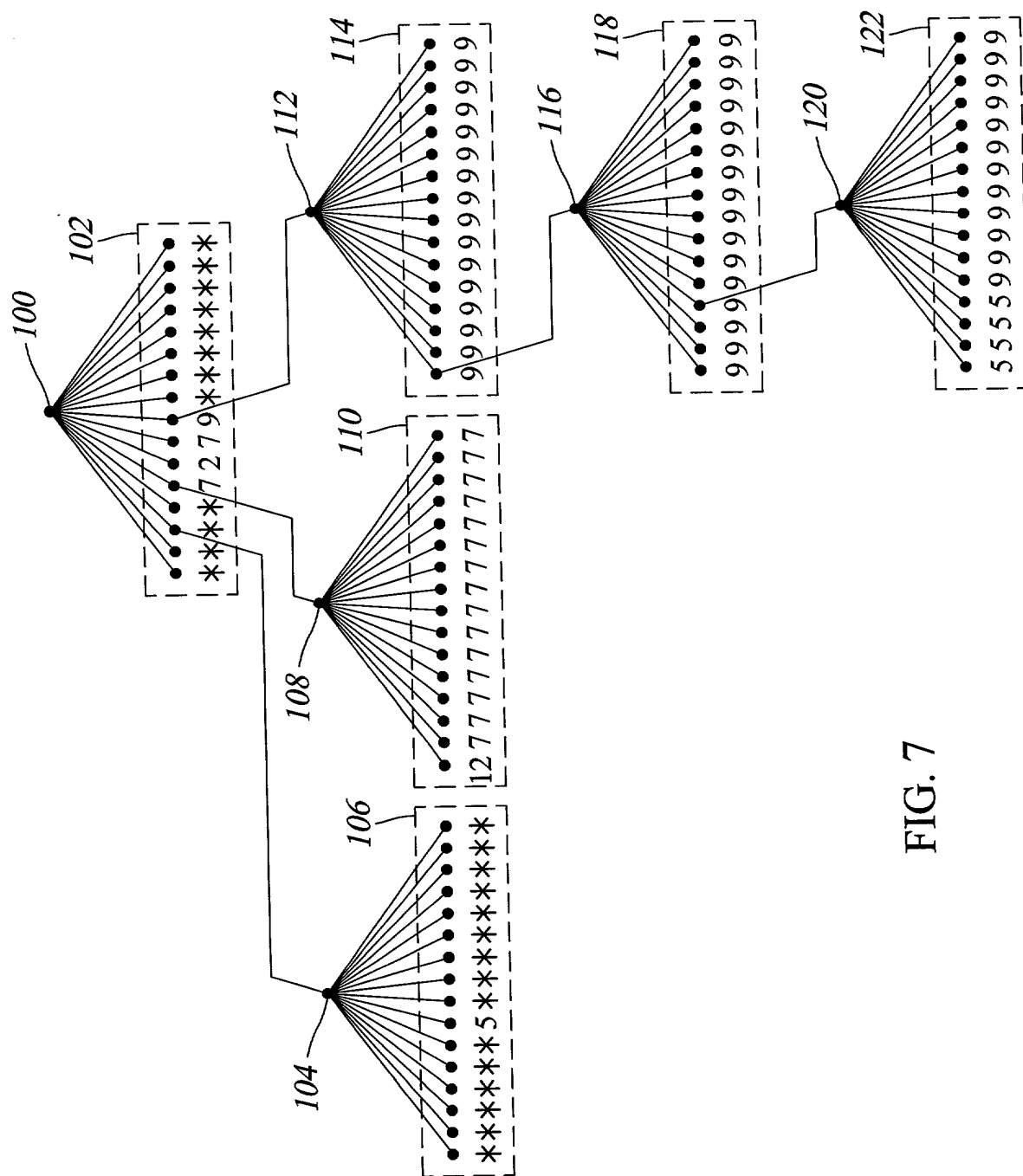


FIG. 7

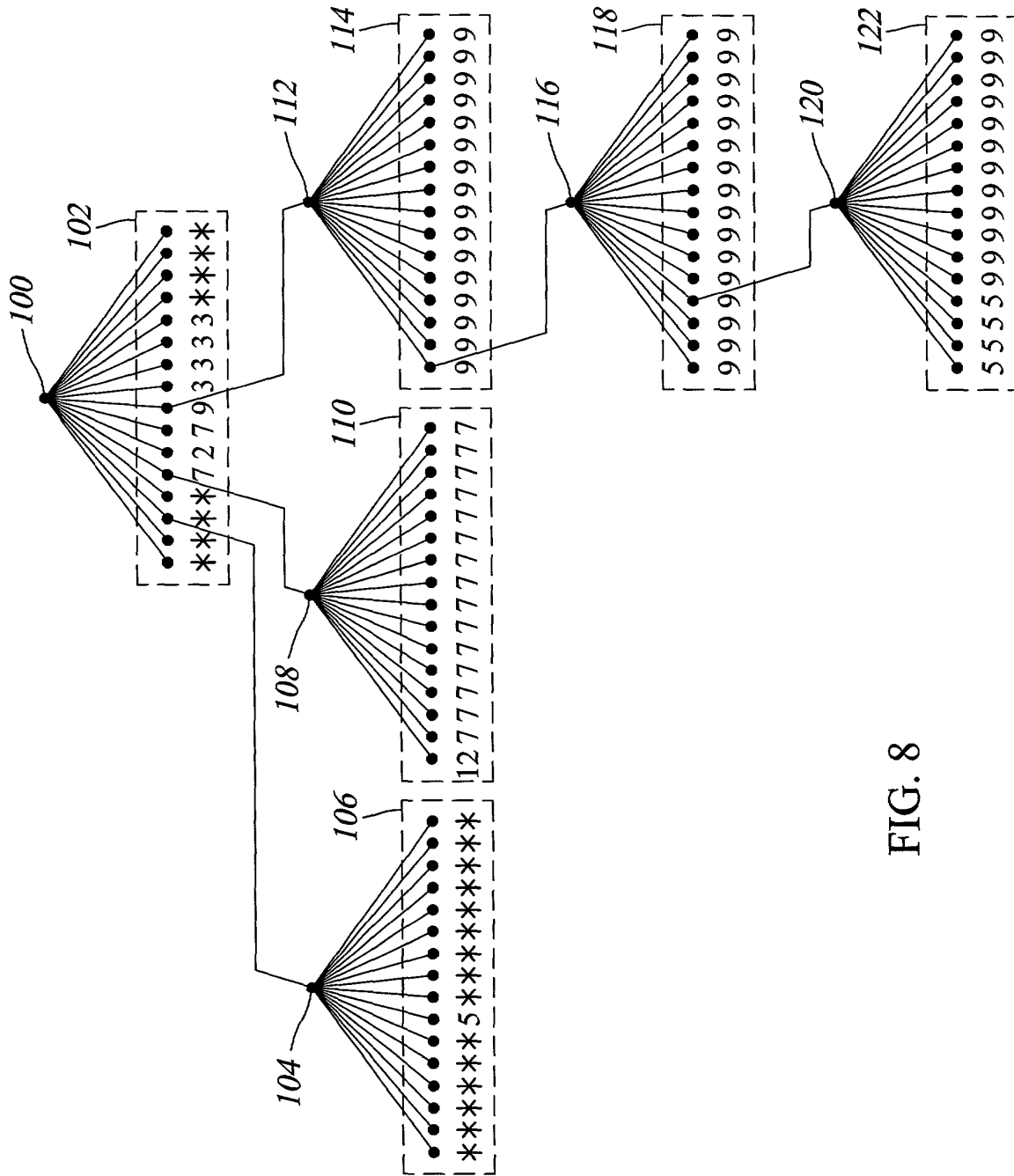


FIG. 8



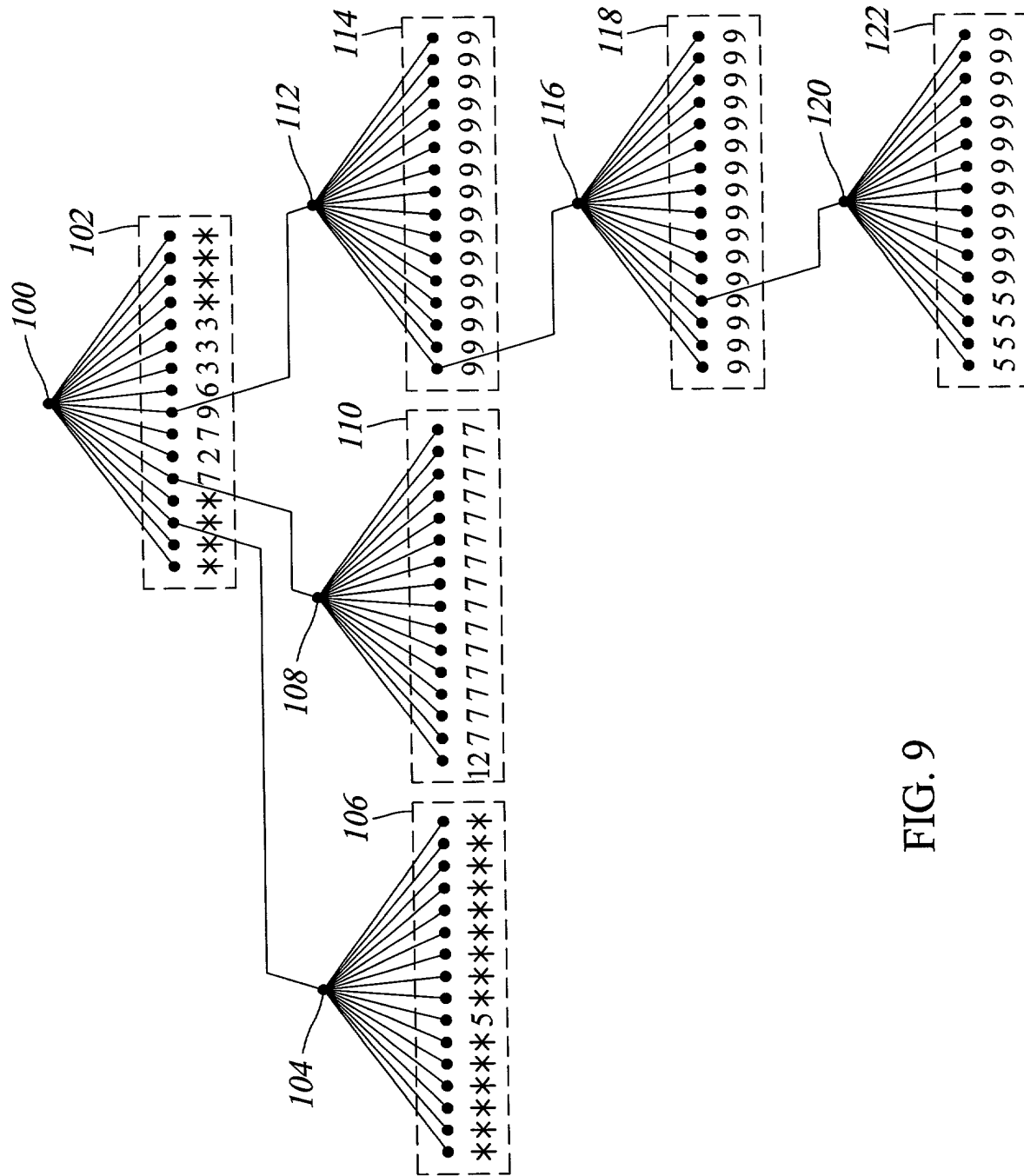


FIG. 9

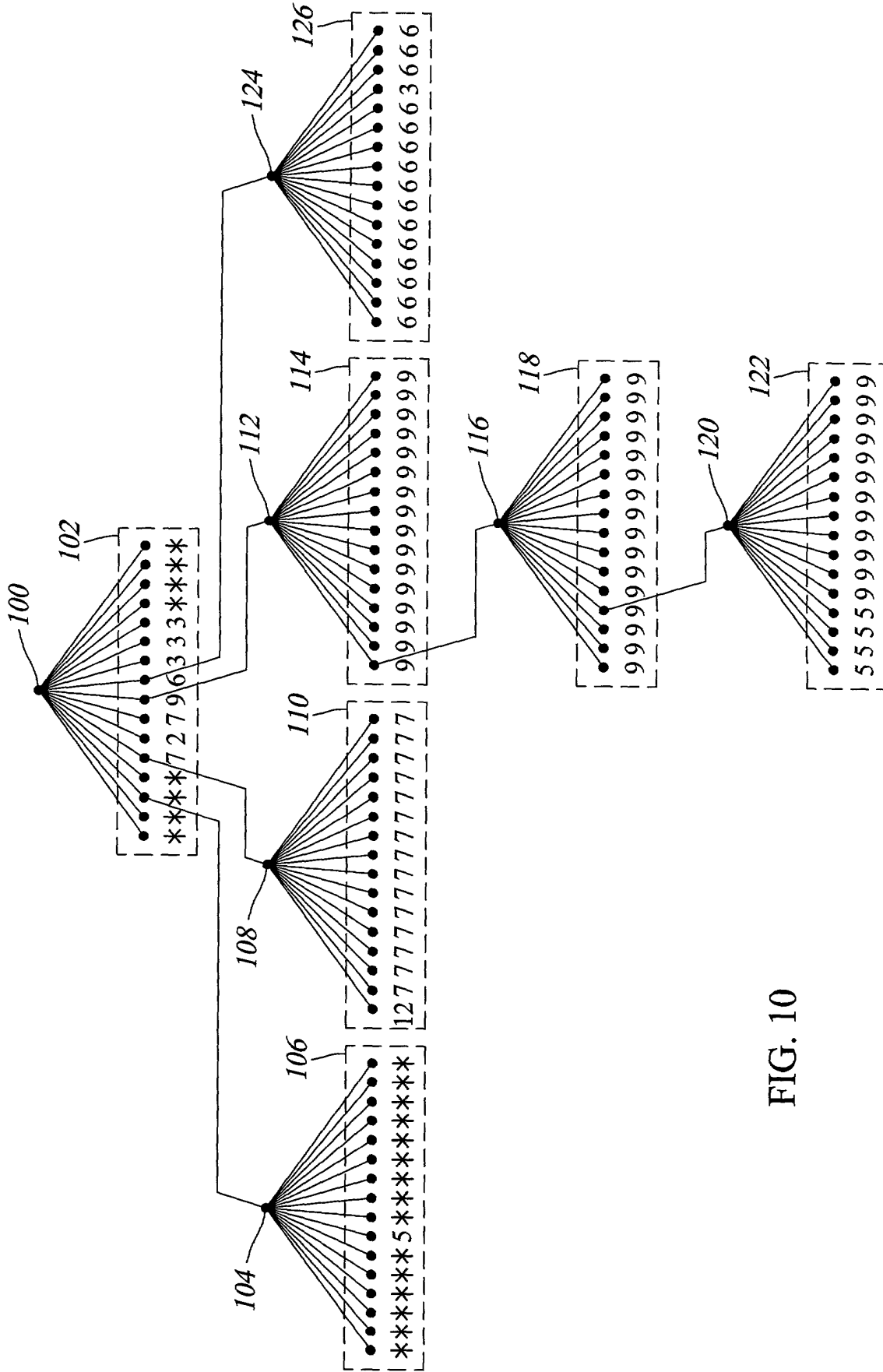


FIG. 10

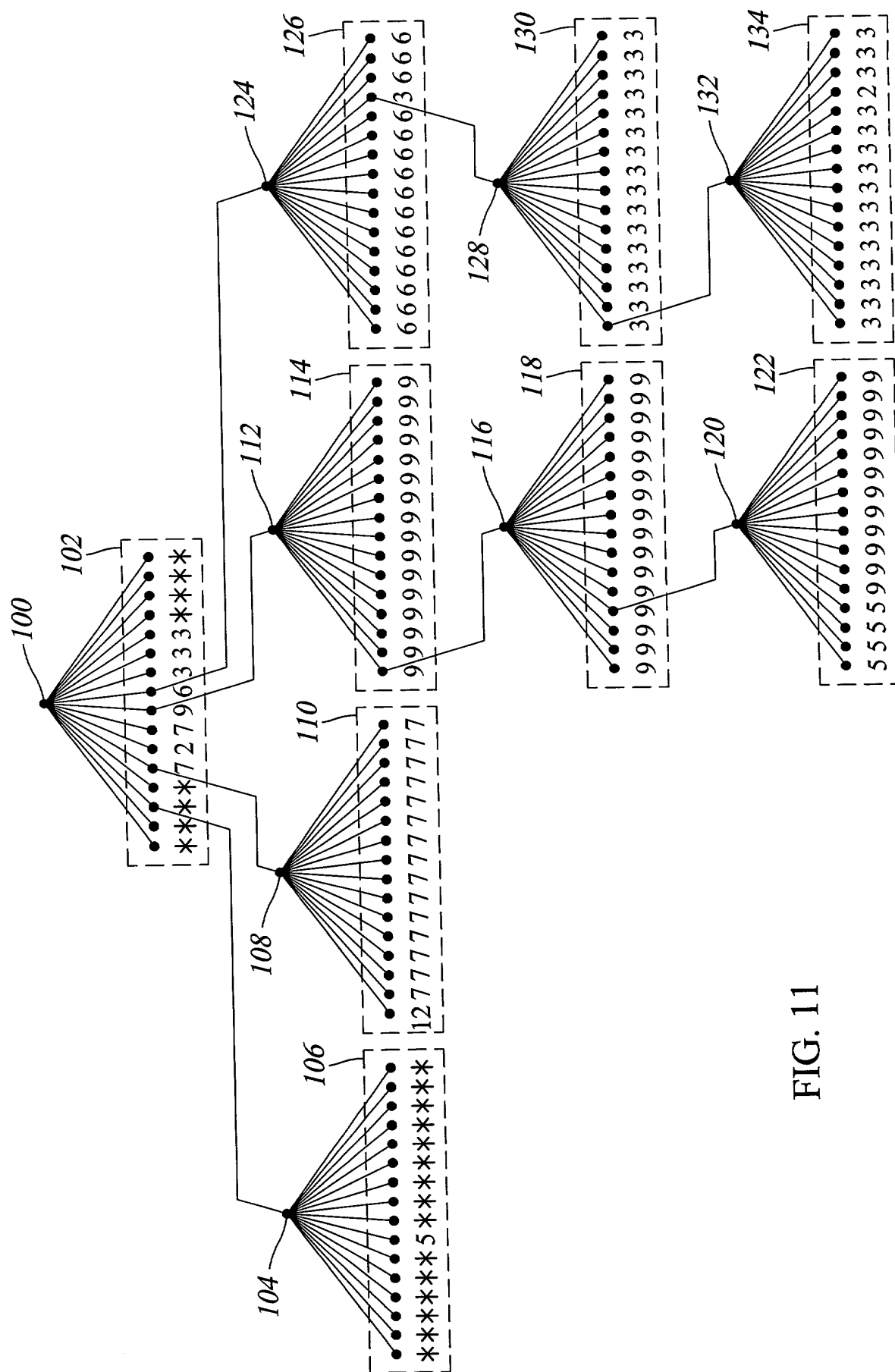


FIG. 11

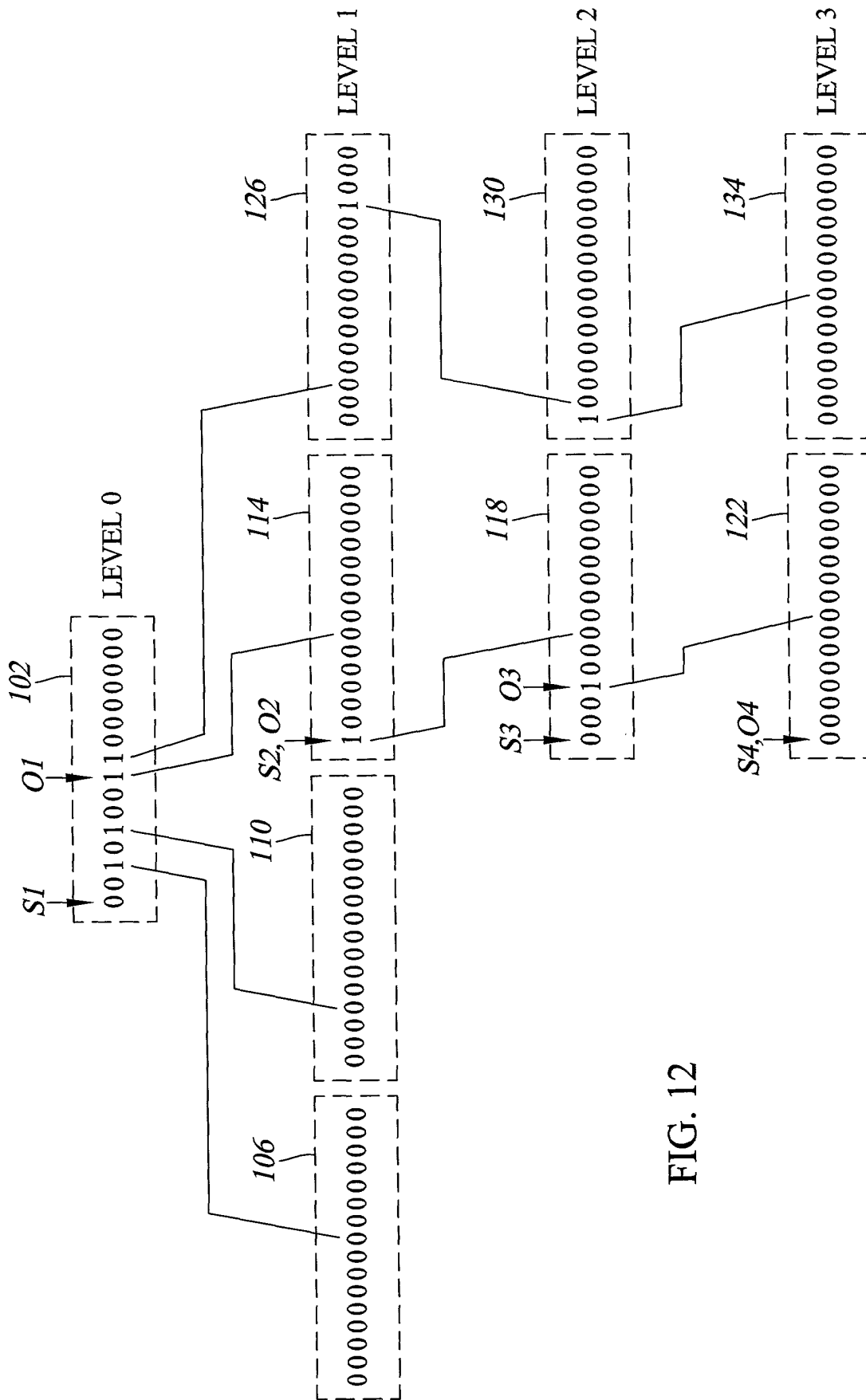


FIG. 12

136

138	****72796333****
140	*****5*****
142	1277777777777777
144	9999999999999999
146	6666666666663666
148	9999999999999999
150	3333333333333333
152	5555999999999999
154	3333333333332333

FIG. 13

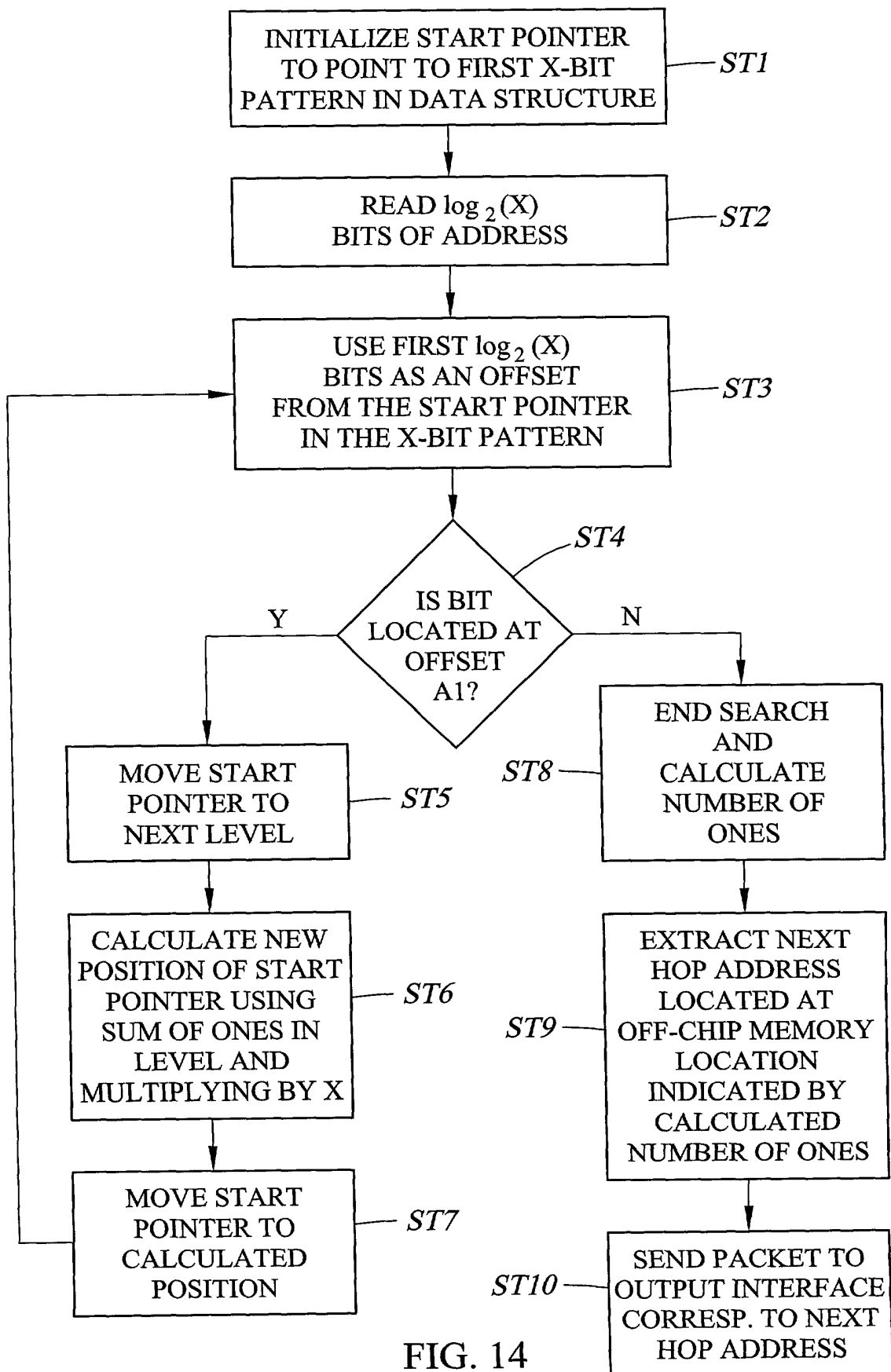


FIG. 14

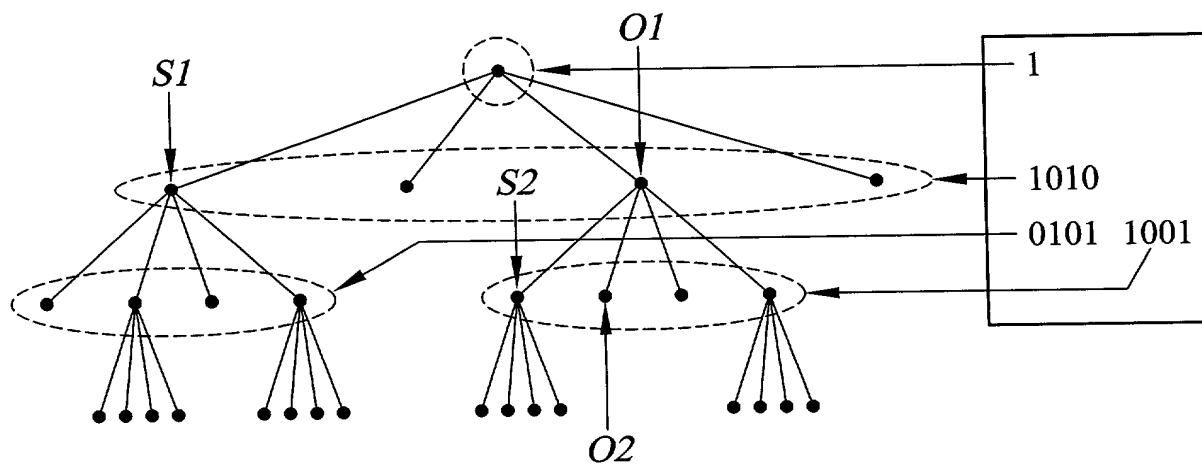


FIG. 15A

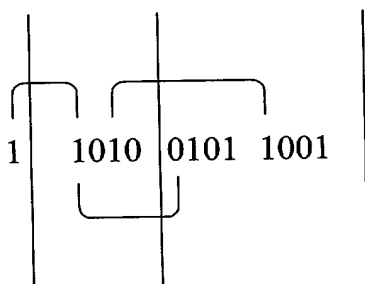


FIG. 15B

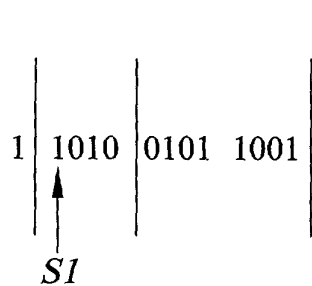


FIG. 16

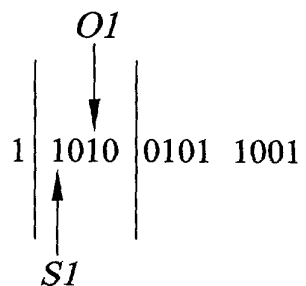


FIG. 17

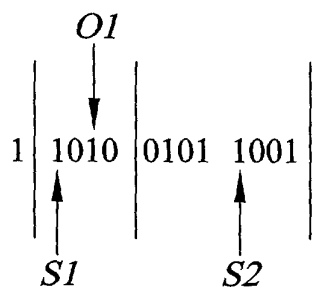


FIG. 18

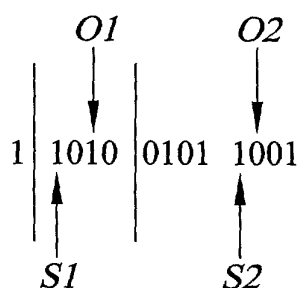


FIG. 19

Sum	Bit Pattern	
0x0000	0x2980	Level 0
0x0000	0x0000 0x0000 0x8000 0x0008	Level 1
0x0000	0x1000 0x8000	Level 2
0x0000	0x0000 0x0000	Level 3

FIG. 20



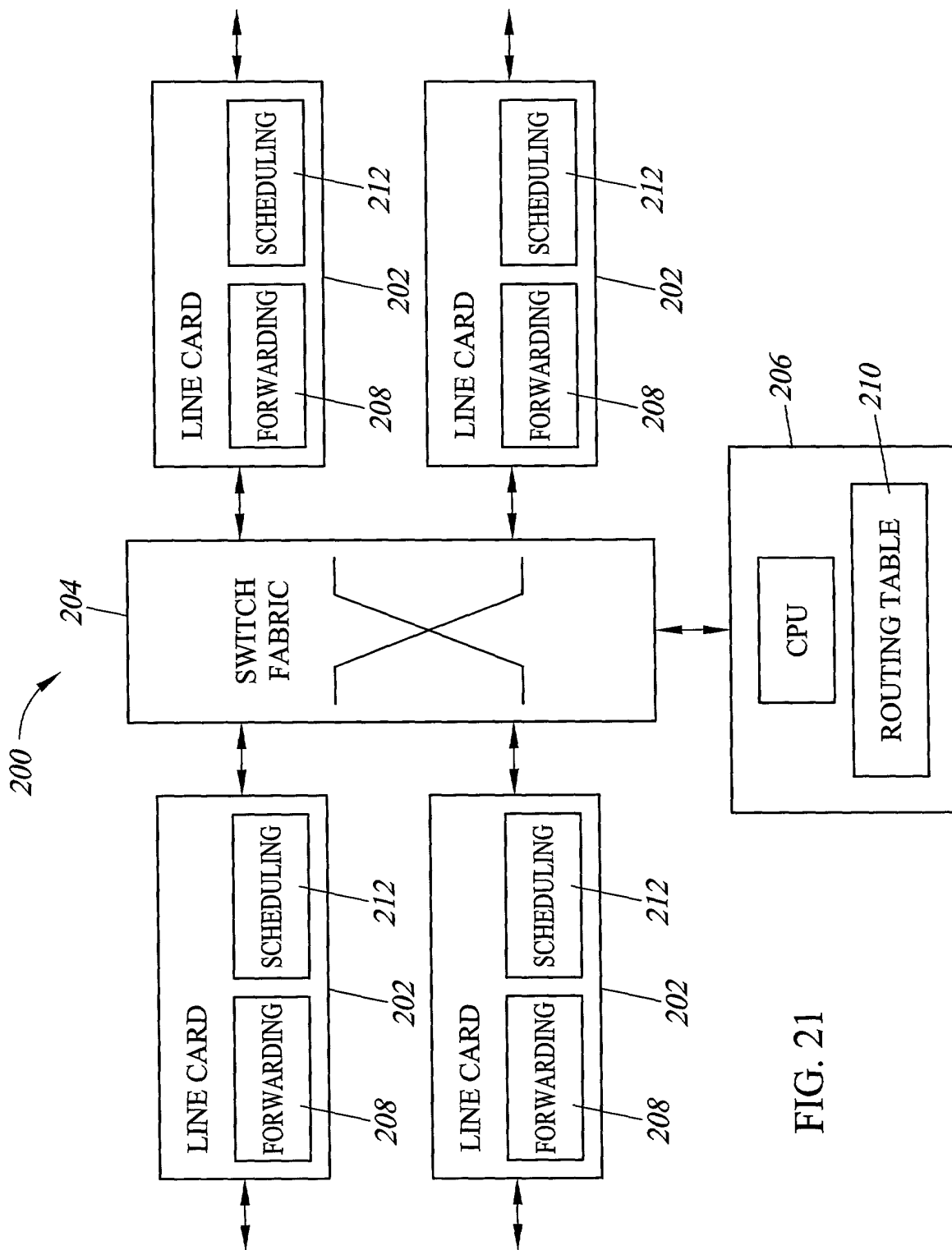


FIG. 21

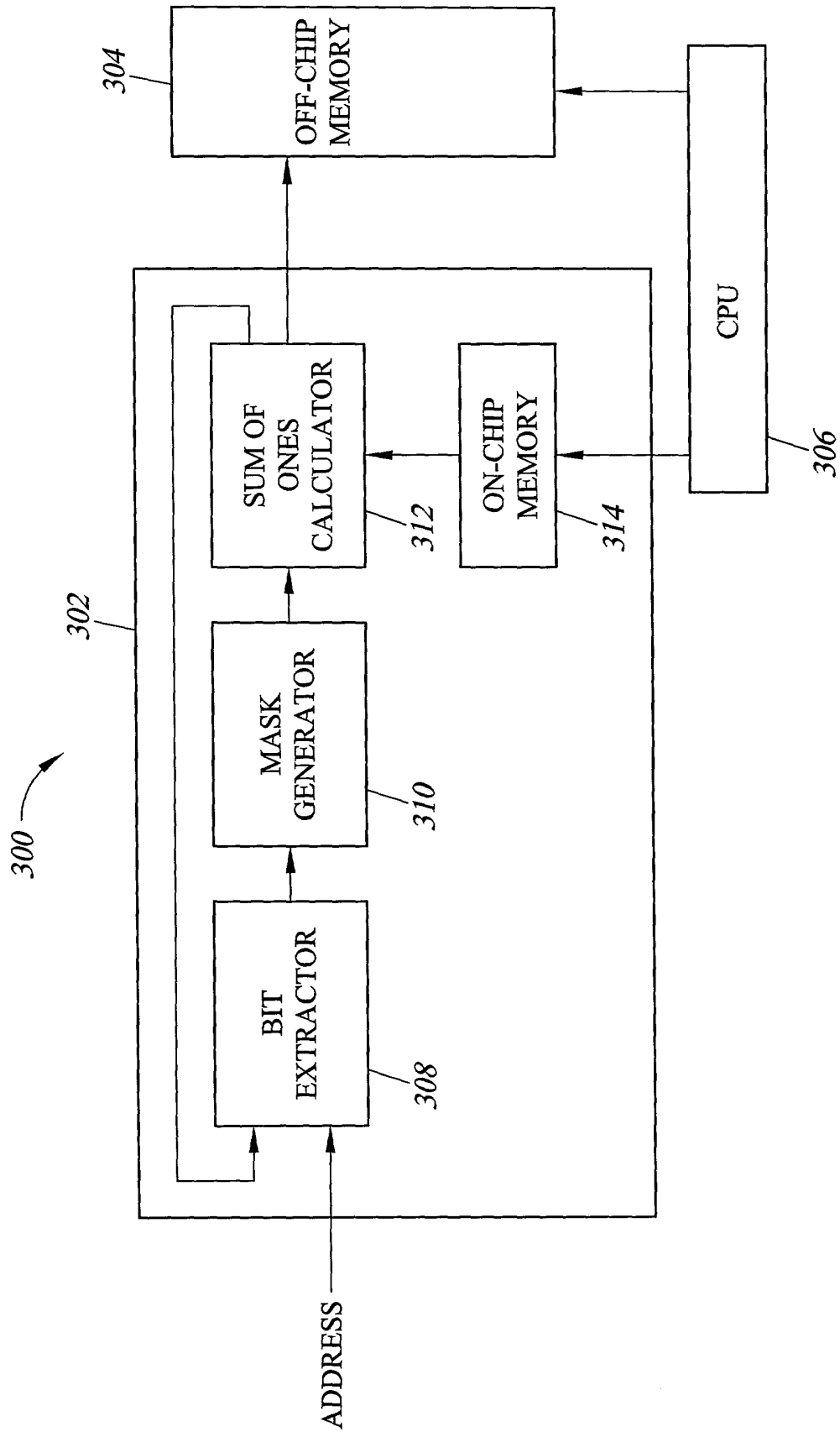


FIG. 22

FIG. 23

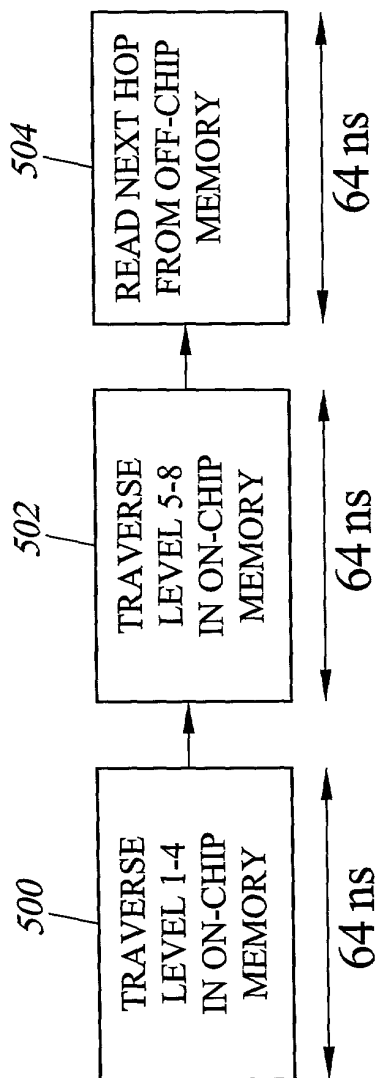


FIG. 24

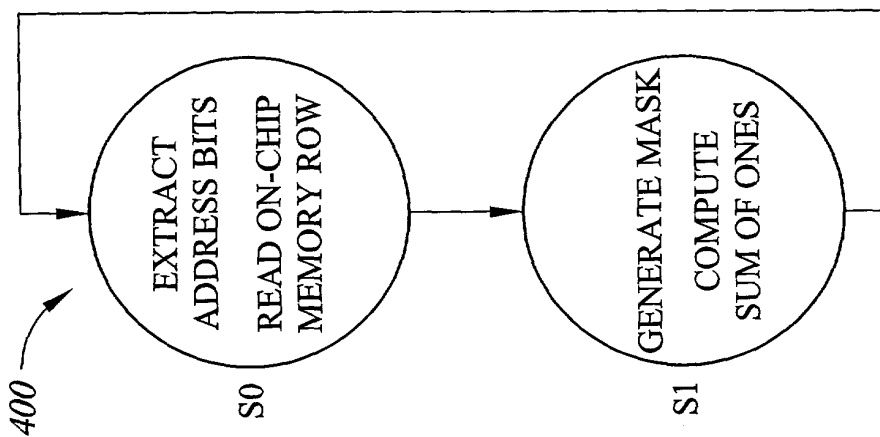


FIG. 23

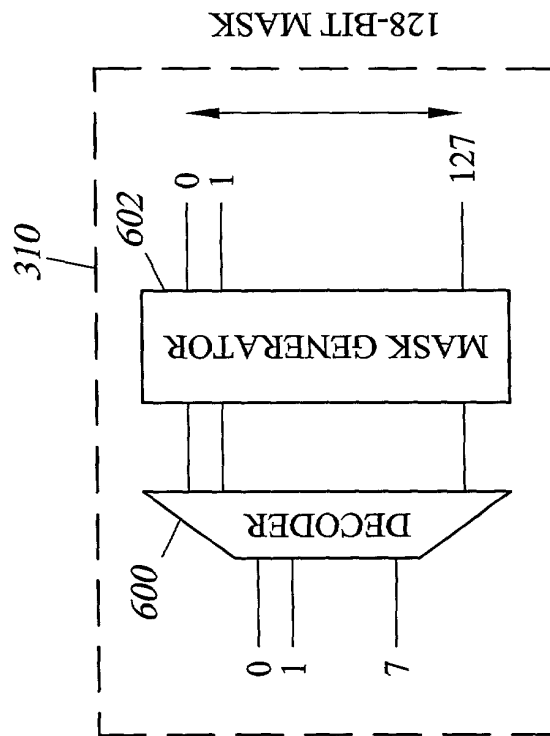


FIG. 25

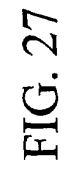


FIG. 26